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Dr. Dong-Won Kim is a Fellow of Logic Technology Development, Semiconductor R&D Center, in charge of logic device development for 3nm and beyond 3nm node. He had worked in Samsung Electronics from 1989 to 1998. After his Ph.D. study at UT Austin, in 2003, he rejoined Samsung Electronics. His research & development focus on the nano scaled device, 3 & 1 dimensional CMOS transistors, and 20nm, 14nm, 10nm, 7nm & 3nm technology node for logic device applications. He has authored or coauthored over 110 publications related to nano scale device, especially FinFET, GAA (Gate All Around) device called MBCFET (Multi-Bridge Channel FET) and Logic applications. He is the holder of over 110 U.S. patents. He had severed on several IEEE conference technical committees including IEDM, SOI conference, ICICDT, VLSI-TSA and Silicon Nanoelectronics Workshop.

He received the B.S. and M.S. degrees from Korea University, Seoul, Korea in 1987 and 1997, respectively, and the Ph.D. degree at the University of Texas, Austin in 2003.