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## **Dr. Chan Hwang**

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Master in Memory Photo Process Development



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Dr. Hwang joined Samsung Electronics Semiconductor R&D Center in 2002 after received Ph.D of Mechanical Design & Production Engineering at Seoul National University where he had got Doctor (1998) and Bachelor (1996) degree as well. Since 2002, He has been developed next-generation photo patterning process especially for DUV/EUV scanner & application technology, overlay metrology & control technology, and Process error budget analysis & improvement in Advanced Photo Technology Division. In 2019, he was appointed master in Memory Photo Process Development. Now, he investigates cost-effective patterning process and research ground-breaking photo technologies for both DRAM and Flash devices, providing critical solution for both R&D center & mass production.